

B	I	N	G	O
reactive system	ASIC	HDL	UART	Programmable logic
FPGA	quantization	Schematic diagram	Actuator	calibration
sample rate	BIOS	FREE	Real-time system	NRE costs
EMC	PCB	VHDL	PLD	System-on-chip
Verilog	I2C	Hardware	RTOS	Bootloader

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Real-time system	Programmable logic	BIOS	calibration	Bootloader
VHDL	NRE costs	PCB	quantization	Actuator
sample rate	UART	FREE	RTOS	FPGA
HDL	Schematic diagram	Verilog	EMC	System-on-chip
ASIC	reactive system	system memory	Firmware	Hardware

B	I	N	G	O
Real-time system	FPGA	System-on-chip	Firmware	Actuator
ASIC	system memory	HDL	sample rate	EMC
quantization	Verilog	FREE	PLD	UART
Bootloader	RTOS	PCB	NRE costs	BIOS
calibration	Programmable logic	Hardware	Schematic diagram	reactive system