

B	I	N	G	O
reactive system	ASIC	HDL	UART	Programmable logic
FPGA	quantization	Schematic diagram	Actuator	calibration
sample rate	BIOS	Real-time system	NRE costs	EMC
PCB	VHDL	PLD	System-on-chip	Verilog
I2C	Hardware	RTOS	Bootloader	system memory

B	I	N	G	O
system memory	FPGA	sample rate	BIOS	UART
Schematic diagram	Firmware	Actuator	reactive system	Programmable logic
EMC	PCB	HDL	Real-time system	NRE costs
ASIC	Hardware	System-on-chip	quantization	Verilog
PLD	Bootloader	RTOS	calibration	I2C

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PCB	HDL	NRE costs	RTOS	EMC
ASIC	reactive system	quantization	Real-time system	PLD
Verilog	Hardware	Bootloader	Schematic diagram	FPGA